

G.H. RAISONI COLLEGE OF ENGINEERING
 (An Autonomous Institute by UGC Act & Affiliated to RTM Nagpur University)
 CRPF Gate No.3, Hingna Road, Dighod Hills, Nagpur-440016 (INDIA)
 DEPARTMENT OF ELECTRONICS ENGINEERING
 Time Table - SECTION WISE

W.e.f. Date :

Semester: III SEM						YEAR: II nd YEAR			
Session: 2018-19						Section :A			
DAYS	1	2	3	4	5	6	7	8	9
	8.30 TO 9.30	9.30 TO 10.30	10.30 TO 11.30	11.30 TO 12.30	12.30 TO 1.30	1.30 TO 2.30	2.30 TO 3.30	3.30 to 4:30	4.30 to 5:30
MON	DSC/E-28/ SS /A2 EDC/E-12/ LPT/A4 PE/C-14/ PMG/A3		EDC VRB E-03	R	MAE&CE RPG E-09	LIB	DSC A1A2/(E-28)/SS A3A4/(E-12)/AG		
TUE	DSC A1A2/(E-28)/SS A3A4/(E-23)/AG		R	EDC VRB E-09	NT NL E-09	PE PMG E-03	SPORT PB/PMG		
WED	DSC/E-28/ SS/A4 EDC/E-12/ LPT /A3 PE/C-26/ PMG/A1		EDC VRB E-07	R	PE PMG E-09	MAE&CE RPG E-03	NT NL E-03	EDC (EXTRA) VRB E-03	
THU	OE	MAE&CE RPG E-09	PE PMG E-03	R	DSC/E-28/SS/A3 EDC/E-12/VRB/A1 PE/C-23 (A)/MMP/A2		NT NL E-03	NT (EXTRA) NL E-03	MAE&CE (EXTRA) RPG E-03
FRI	OE	EDC-TUT MMP/VRB E-03/E-36	MAE&CE RPG E-03	R	NT NL E-09	DSC/E-28/SS/A1 EDC/E-12/LPT/A2 PE/C-23(A)/PMG/A4		PE PMG E-09	PE (EXTRA) PMG E-09
SAT	GP-II Foreign Language								

SUBJECT: CE-COMMUNICATION ELECTRONICS,EDC-ELECTRONIC DEVICES & CIRCUITS,NT-NETWORK THEORY,DSC-DATA STRUCTURES USING C,M-III-APPLIED MATH
 FACULTY: MMP-PROF.M.M.PATHAN, LPT-PROF.L.P.THAKRE,RPG-PROF.R.P. GUPTA,SS-PROF.S.SORTE,SG-PROF. S. GAJBHAYE,SB-S.BHONGADE,NP-NEHA PUROHIT

M.M. Pathan
 Time Table Incharge

DR. V.R.Bora
 Head of Department

DR.S.S.DORLE
 Dean Academic

DR.P.R.BAJAJ
 Director

(s. P.R. Bajaj)

G.H. RAISONI COLLEGE OF ENGINEERING
 (An Autonomous Institute by UGC Act & Affiliated to RTM Nagpur University)
 CRPF Gate No.3, Hingna Road, Dighob Hills, Nagpur-440016 (INDIA)
DEPARTMENT OF ELECTRONICS ENGINEERING
 Time Table - SECTION WISE


W.e.f. Date :


Semester: III SEM						YEAR: II nd YEAR			
Session: 2018-19						Section :B			
DAYS	1	2	3	4	5	6	7	8	9
	8.30 TO 9.30	9.30 TO 10.30	10.30 TO 11.30	11.30 TO 12.30	12.30 TO 1.30	1.30 TO 2.30	2.30 TO 3.30	3.30 to 4:30	4.30 to 5:30
MON	CE PB E-09	MAE&CE RPG E-09	EDC/E-12/MMP/B1 CE/E-13/PB/B3		R	EDC MMP E-04	EDC MMP E-09	CAO SKP E-09	
TUE	NT NL E09	CAO SKP E-09	EDC/E-12/MMP/B3 CE/E-13/PB/B1		R	CE PB E-07	SPORT LPT/MMP		
WED	NT NL E09	CAO SKP E-04	EDC MMP E-04	MAE&CE RPG E-04	R	CE-TUT PB/MMP E-04/E-36	EDC-TUT MMP/VRB E-04/E-36	NT (EXTRA) NL E-04	
THU	OE	CE/E-13/PB/B4 EDC/E-12/MMP/B2		NT NL E04	R	MAE&CE RPG E-04	CAO SKP E-04	EDC (EXTRA) MMP E-04	CE (EXTRA) PB E-04
FRI	OE	CE/E-13/PB/B2 EDC/E-12/PMG/B4		NT NL E-07	R	CE PB E-04	MAE&CE RPG E-04	LIB LPT	MAE&CE(EXTRA) RPG E-04
SAT									

SUBJECT: CE-COMMUNICATION ELECTRONICS,EDC-ELECTRONIC DEVICES & CIRCUITS,NT-NETWORK THEORY,DSC-DATA STRUCTURES USING C,M-III-APPLIED MATH
 FACULTY: MMP-PROF.M.M.PATHAN, TGD-PROF.T.G.DEOTALE,RPG-PROF.R.P. GUPTA,PMG-PROF. P.M. GHUTKE,SKK-PROF.S.K. KHARE,AKM-PROF.A.K. MAURYA,AS-

M.M. Pathan

 Time Table Incharge


 DR. V.R. Bora
 Head of Department


 DR.S.S.DORLE
 Dean Academic


 DR. P.R. BAJAJ
 Director

(Ms. P.R. Bajaj)

G.H. RAISONI COLLEGE OF ENGINEERING
 (An Autonomous Institute by UGC Act & Affiliated to RTM Nagpur University)
 CRPF Gate No.3, Hingna Road, Dighod Hills, Nagpur-440016 (INDIA)
 DEPARTMENT OF ELECTRONICS ENGINEERING
 Time Table - SECTION WISE


W.e.f. Date :


Semester: V SEM					YEAR: III rd YEAR				
Session: 2018-19					Section :A				
DAYS	1 8.30 TO 9.30	2 9.30 TO 10.30	3 10.30 TO 11.30	4 11.30 TO 12.30	5 12.30 TO 1.30	6 1.30 TO 2.30	7 2.30 TO 3.30	8 3.30 to 4:30	9 4.30 to 5:30
MON		CSE PPJ E-07	DCOM NPW E-07	S&S-TUT LPT/VRB E-09	MBS/TH/SVB/A1,A2 E-17 MBS/TH/SNJ/A3,A4/ E-28		GP-IV SB/JE E-07/E-04		
TUE		CSE PPJ E-07	DCOM NPW E-07	GP-IV SB/JE E-07/E-04	R	MBS/TH/SVB/A1,A2 E-17 MBS/TH/SNJ/A3,A4/ E-28		MBS/E-17/BBS /A2 CSE/C-14/HS /A4 WS/E-11/NPW/A3	
WED		MBS/E-17/SNJ/A1 CSE/C-14/HS /A3 WS/E-11/NPW/A2		S&S LPT E-07	R	CSE PPJ E-07	DCOM (EXTRA) NPW E-07		
THU	OE E-07	S&S LPT E-07	DCOM NPW E-07	R	CSE PPJ E-07	MBS/E-17/KND/A3 CSE/C-11/HS/A1 WS/E-11/NPW/A4	CSE(EXTRA) PPJ E-07		
FRI	OE E-07	S&S LPT E-07	DCOM NPW E-07	S&S (EXTRA) LPT E-09	R	MBS/E-17/KND/A4 CSE/C-11/HS/A2 WS/E-11/NPW/A1		SPORT SNJ/THR	
SAT									

SUBJECT: MBS-MICROPROCESSOR BASED SYSTEM, STA-SWITCHING THEORY AND AUTOMATA, S&S-SIGNAL AND SYSTEM, CSE-CONTROL SYSTEM ENGINEERING, DCOM-
 FACULTY: SNJ-PROF.S.N.JOSHI, WVP-PROF.W.V.PATIL, RKT-PROF.R.K.TIWARI, SKK-PROF.S.KHARE, NPW-PROF.N.P.WYAWAHARE, SD-PROF.-S.DUDHE, YNK-

M.M. Pathan

Time Table Incharge


 DR. V.R. Bora
 Head of Department


 DR. S.S. DORLE
 Dean Academic


 DR. P.R. BAJAJ
 Director

(Ms. P.R. Bajaj)

G.H. RAISONI COLLEGE OF ENGINEERING
 (An Autonomous Institute by UGC Act & Affiliated to RTM Nagpur University)
 CRPF Gate No.3, Hingna Road, Digdoh Hills, Nagpur-440016 (INDIA)
 DEPARTMENT OF ELECTRONICS ENGINEERING
 Time Table - SECTION WISE

W.e.f. Date :

Semester: V SEM						YEAR: III rd YEAR			
Session: 2018-19						Section :B			
DAYS	1	2	3	4	5	6	7	8	9
	8.30 TO 9.30	9.30 TO 10.30	10.30 TO 11.30	11.30 TO 12.30	12.30 TO 1.30	1.30 TO 2.30	2.30 TO 3.30	3.30 to 4:30	4.30 to 5:30
MON	MBS/E-17/SVB/B2 WS/E-11/KND/B1		S&S LPT E-09	GP-IV SB/RG E-04/E-07	R	STA (EXTRA) BBS E-09	MBS-TUT SVB/LS E-03/E-36		
TUE	MBS/E-17/SVB/B4 WS/E-11/KND/B2		STA BBS E-09	LIB	R	S&S (EXTRA) LPT E-09	DCOM PB E-09	GP-IV JE/SB E-03/E-09	
WED			S&S LPT E-09	MBS/E-17/SVB/B3 WS/E-11/KND/B4		MBS/TH/SVB/B2,B3/E-28 MBS/TH/SNJ/B1,B2/E-17	DCOM PB E-07		
THU	OE E-07	LIB	STA BBS E-04	MBS/TH/SVB/B2,B3/E-23(A) MBS/TH/SNJ/B1,B2/E-17		DCOM (EXTRA) PB E-09	S&S- TUT LPT/VRB E-09/E-36	MBS/E-17/SVB/B1 WS/E-11/KND/B3	
FRI	OE E-07	DCOM PB E-09	S&S LPT E-09	STA BBS E-04	R	DCOM PB E-09	SPORT/SVB		
SAT									

SUBJECT: MBS-MICROPROCESSOR BASED SYSTEM, EEIM-ENGINEERING ECONOMICS & INDUSTRIAL MANAGEMENT, S&S-SIGNAL AND SYSTEM, CSE-CONTROL SYSTEM
 FACULTY: SVB-PROF.S.V.BHALERAO, WVP-PROF.W.V.PATIL, SNJ-PROF.S.N.JOSHI, WVP-PROF.L.P.THAKARE, NPW-PROF.N.P.WYAWADARE, MM-M. MENON, NF-NUPUR

M.M. Pathan
 Time Table Incharge

DR. V.R. Bora
 Head of Department

DR. S.S. DORLE
 Dean Academic

DR. P.R. BAJAJ
 Director
 (Ms. P.R. Bajaj)

G.H. RAISONI COLLEGE OF ENGINEERING
 (An Autonomous Institute by UGC Act & Affiliated to RTM Nagpur University)
 CRPF Gate No.3, Hingna Road, Dighoh Hills, Nagpur-440016 (INDIA)
 DEPARTMENT OF ELECTRONICS ENGINEERING
 Time Table - SECTION WISE

W.e.f. Date :

Semester: VII SEM						YEAR: IV th YEAR			
Session: 2018-19						Section :A			
DAYS	1	2	3	4	5	6	7	8	9
	8.30 TO 9.30	9.30 TO 10.30	10.30 TO 11.30	11.30 TO 12.30	12.30 TO 1.30	1.30 TO 2.30	2.30 TO 3.30	3.30 to 4:30	4.30 to 5:30
MON			CMOS KND E-03	DIP SDG E-03					
TUE			CMOS KND E-03	DIP SDG E-03					
WED		DIP SDG E-03	CMOS KND E-03						
THU	ES/VII-ETRX-A/BBS/A2/E-17 DIP/VII-ETRX-A/SVB/A1/E-28								
FRI		DIP SDG E-03	ES/VII-ETRX-A/KND/A3/E-17 DIP/VII-ETRX-A/SSD/A2/E-28		ES/TH/SSD/A1A2/E-23 ES/TH/SNJ/A3A4/28				
SAT		ES/VII-ETRX-A/SVB/A1/E-23 DIP/VII-ETRX-A/LPT/A3/E-12		ES/TH/SSD/A1A2/E-23 ES/TH/SNJ/A3A4/12					

SUBJECT: CN-COMPUTER NETWORKS, CMOS-CMOS & VLSI DESIGN, E-II-ELECTIVE-II (PDT-PROGRAMMABLE DEVICES AND TESTING, OC-OPTICAL COMMUNICATION, PLC & FACULTY : SSD-S.S. DORLE, KND-PROF. K.N. DEKATE, BBS-M.M. Pathan, WVP-PROF. W.V. PATIL, LS-PROF. L. SHARMA, SDG-S.D. GIRIPUNJE, PJM-P. J. MANWAR, SKK-S.K. KHARE, MK-

M.M. Pathan

Time Table Incharge

DR. V.R. Bora

Head of Department

DR. S.S. DORLE

Dean Academic

DR. P.R. BAJAJ

Director

(Ms. P.R. Bajaj)

G.H. RAISONI COLLEGE OF ENGINEERING
 (An Autonomous Institute by UGC Act & Affiliated to RTM Nagpur University)
 CRPF Gate No.2, Hingna Road, Dighoh Hills, Nagpur-440016 (INDIA)
DEPARTMENT OF ELECTRONICS ENGINEERING
 Time Table - SECTION WISE

W.e.f. Date :

Semester: VII SEM					YEAR: IV th YEAR				
Session: 2018-19					Section :B				
DAYS	1	2	3	4	5	6	7	8	9
	8.30 TO 9.30	9.30 TO 10.30	10.30 TO 11.30	11.30 TO 12.30	12.30 TO 1.30	1.30 TO 2.30	2.30 TO 3.30	3.30 to 4:30	4.30 to 5:30
MON		CMOS MMP E-04	ES/TH/SSD/B1B2/E-17 ES/TH/SNJ/B3B4 /E-23		DIP PHR E-04				
TUE						CMOS MMP E-04	DIP PHR E-04	ES/TH/SSD/B1B2/E-28 ES/TH/SNJ/B3B4/E-23	
WED			ES/VII-ETRX-B/BBS/B2/E-23(A) DIP/VII-ETRX-A/SVB/B3/E-28						
THU			CMOS MMP E-09	DIP PHR E-09					
FRI		ES/VII-ETRX-B/BBS/B3/E-17 DIP/VII-ETRX-B/PHR/B1/E-28		DIP PHR E-03					
SAT		ES/VII-ETRX-B/BBS/B1/E-17 DIP/VII-ETRX-B/PHR/B2/E-28							

SUBJECT: CN-COMPUTER NETWORKS, CMOS-CMOS & VLSI DESIGN, E-II-ELECTIVE-IPDT-PROGRAMMABLE DEVICES AND TESTING, OC-OPTICAL COMMUNICATION, PLC &
 FACULTY : SSD-S.S DORLE, KND-PROF.K.N.DEKATE, BBS-M.M. Pathan, WVP-PROF.W.V.PATIL, LS-PROF.I.SHARMA, SDG-S.D.GIRIPUNJE, PJM-P.J. MAHAPATRA, SKK-S.K.KHARE, MK-

M.M. Pathan

Time Table Incharge

Pathan
 DR. V.R.Bora
 Head of Department

Dorle
 DR.S.S.DORLE
 Dean Academic

Bajaj
 DR.P.R.BAJAJ
 Director

(Ms. P.R. Bajaj)