



NOTIFICATION

It is notified for general information of all the concerned that the **B.E. in Electronics Engineering and M-Tech. in VLSI and ETRX** students who were admitted in this College prior to grant of Autonomy and pursuing the course as per Rashtrasant Tukadoji Maharaj Nagpur University, Nagpur scheme and could not clear the subjects of the above said courses as per R.T.M. Nagpur University, Nagpur scheme within the given point of time limit as stated in our earlier **Notification No. GHRCE/Autonomy/ ACAD/2010-2011/176 Dated 25/09/2010** and further stated below in **Para 1 shall then be absorbed** in the new **B.E. in Electronics Engineering and M-Tech. in VLSI and ETRX** course as per G.H.Raisoni College of Engineering Scheme and Syllabus under Autonomous status introduced from the academic session 2010-11.

1. Since the last chance of **Ist year B.E (U.G) and Ist and IInd Semester (P.G)** courses as per RTM Nagpur University Scheme were exhausted in Winter 2011, the Academic Council of G.H.Raisoni College of Engineering in its meeting held on 01.03.2012 **given one more additional chance of Summer 2012** to first year B.E. (U.G.) and **Ist and IInd Semesters of P.G.** students to appear in Examinations as per scheme and ordinance of R.T.M. Nagpur University.
2. Academic Council in its same meeting dated 01.03.2012, approved the enclosed equivalence and absorption scheme considering the recommendations of Board of Studies of respective departments with incorporating following points:
 - a) Students absorbed in Autonomous course shall required to clear some additional subjects which are already covered in earlier semesters, as Audit Course.
 - b) Audit courses shall be offered as per the approval of respective Board of Studies.
 - c) Students must acquire minimum pass grade in each audit course and they have to clear such courses before 8th Semester.
 - d) For Students who are admitted in respective ordinance, same ordinance shall be followed for calculation of CGPA for award of their degree. For **Engineering Students** admitted in Ordinance having degree to be awarded on last 4 semesters their CGPA will be calculated from 5th to 8th semester. If such students are absorbed in or after 6th semester their previous semester marks will be converted to CGPA based on absolute grading and degree will be awarded by calculating CGPA from 5th to 8th semester. However, students admitted in the Ordinance of degree based on last 2 semester (Students admitted before 2004) their CGPA will be calculated for 7th & 8th semester only.
 - e) Students who are absorbed in 4th semester will have to clear the audit courses of 3rd semester during 5th semester.
 - f) Students absorbed in 5th semester will have to clear the audit courses of 3rd semester during 5th semester and audit courses of 4th semester during 6th semester.
 - g) Students admitted in 6th semester will have to clear the audit courses of 3rd to 5th semester during 6th & 7th semester.

Absorption Scheme in Electronics Engineering Branch of B.E. IIIrd and IVth Semester and M.Tech Ist, IInd, IIIrd and IVth Semester in VLSI and ETRX are enclosed herewith:

Third Semester B.E.**Branch:- Electronics Engineering**

Sr. No.	Subject Code	Name of Subject as per R.T.M.N.U. Syllabus.	Subject Code	Name of Subject as per G.H.R.C.E. Syllabus Under Autonomous Status.	Equivalence	Remarks
1	NAML201	Mathematics –III	BAML201	Applied Mathematics – III	Yes	-
2	NECL201 NECP201	Electronic Devices & Circuits	BECL 201 BECP 201	Electronic Devices & Circuits	Yes	-
3	NEEL201	Network Theory	BEEL201	Network Theory	Yes	-
4	NECL203 NECP203	C & Data Structure	BCSL201 BCSP201	Data Structures using C	Yes	-
5	--	--	MBL102	General Proficiency-II	-	Audit Course
6	--	--	BECL 202 BECP 202	Communication Electronics	-	Audit Course
7	NECL202 NECP202	Electronic Measurement	--	--	-	Exempted

AUDIT COURSES

Sr. No.	Subject Code	Name of Subject
1	BECL202 BECP202	Communication Electronics (T/P)
2	MBL102	General Proficiency-II

EXEMPTED COURSES

Sr. No.	Subject Code	Name of Subject
1	NECL202 NECP202	Electronic Measurement (T/P)

Sr. No.	Subject Code	Name of Subject as per R.T.M.N.U. Syllabus.	Subject Code	Name of Subject as per G.H.R.C.E. Syllabus Under Autonomous Status.	Equivalence	Remarks
1	--	--	BEEL 310 BEEP 310	Power Electronics	-	Audit Course
2	--	--	BCSL 202	Computer Architecture & Organization	-	Exempted
3	--	--	BECL 301 BECP 301	Digital System Design	-	Audit Course
4	NECL206	Electro Magnetic Fields	BECL 205	Field Theory	Yes	-
5	--	--	BECL 302 BECP 302	Analog Systems & Design	-	Audit Course
6	--	--	BECP 206	Modeling & Simulation	-	Audit Course
7	--	--	MBL103	General Proficiency-III	-	Audit Course
8	NAML301	Mathematics –IV	--	--	-	Exempted
9	NECL204 NECP204	Digital Circuit	--	--	Equivalent to BECL105 & BECP105 Basic Electronics of 1 st /II nd Sem. BE Autonomous	Equivalent
10	NECL205	Electronic Engineering Materials & Components	--	--	-	Exempted
11	NEEL292 NEEP292	Basic Electrical Machines	--	--	-	Exempted

AUDIT COURSES		
Sr. No.	Subject Code	Name of Subject
1	BEEL310 BEEP310	Power Electronics (T/P)
2	BECL301 BECP301	Digital System Design (T/P)
3	BECL302 BECP302	Analog Systems & Design (T/P)
4	BECP206	Modeling & Simulation (P)
5	MBL103	General Proficiency-III

EXEMPTED COURSES		
Sr. No.	Subject Code	Name of Subject
1	NAML301	Mathematics –IV
2	NECL205	Electronic Engineering Materials & Components (T)
3	NEEL292 NEEP292	Basic Electrical Machines (T/P)
4	BCSL202	Computer Architecture & Organization (T)

First Semester M-Tech. VLSI
Branch:- Electronics Engineering

Sr. No.	Subject Code	Name of Subject as per R.T.M.N.U. Syllabus.	Subject Code	Name of Subject as per G.H.R.C.E. Syllabus Under Autonomous Status.	Equivalence	Remarks
1	NVLL501	VLSI subsystem Design	VL5L501	VLSI subsystem Design	Yes	-
2	NVLL502	Modeling of Digital System-I	VL5L502	Modeling of Digital System-I	Yes	-
3	NVLL503	Switching Theory & Automata	VL5L503	Switching Theory & Automata	Yes	-
4	NVLL504	Advanced Digital signal processing	VL5L504	Advanced Digital signal processing	Yes	-
5	NVLL505	Embedded System -I	VL5L505	Embedded System -I	Yes	-
6	NVLP501	VLSI subsystem Design	VL5P518	Laboratory-I	Yes	-
7	NVLP502	Modeling of Digital System-I				
8	NVLP504	Advanced Digital signal processing				
9	NVLP505	Embedded System -I				

Second Semester M-Tech. VLSI
Branch:- Electronics Engineering

Sr. No.	Subject Code	Name of Subject as per R.T.M.N.U. Syllabus.	Subject Code	Name of Subject as per G.H.R.C.E. Syllabus Under Autonomous Status.	Equivalence	Remarks
1	NVLL506	Analog VLSI Design	VL5L506	Analog VLSI Design	Yes	-
2	NVLL507	Modeling of Digital System-II	VL5L507	Modeling of Digital System-II	Yes	-
3	NVLL508	VLSI Signal Processing	VL5L508	VLSI Signal Processing	Yes	-
4	NVLL509	Digital Image Processing	VL5L509	Advanced Digital Image Processing	Yes	-
5	NVLP506	Analog VLSI Design	VL5P519	Laboratory-II	Yes	-
6	NVLP507	Modeling of Digital System-II				
7	NVLP509	Digital Image Processing				
8		Elective -I		Elective -I		
	NVLL510	Advanced Computer Architecture,	VL5L510	Advanced Computer Architecture,	Yes	-
	NVLL511	Low Power VLSI Design	VL5L511	Low Power VLSI Design	Yes	-
	NVLL512	Embedded System –II	VL5L512	Embedded System –II	Yes	-

Third Semester M-Tech. VLSI

Branch:- Electronics Engineering

Sr. No.	Subject Code	Name of Subject as per R.T.M.N.U. Syllabus.	Subject Code	Name of Subject as per G.H.R.C.E. Syllabus Under Autonomous Status.	Equivalence	Remarks
1	NVLL513	VLSI TESTING	VL5L513	VLSI Testing	Yes	-
2	NVLP514	Seminar	VLSP514	Seminar	Yes	-
3		Elective – II		Elective – II		
	NVLL515	Mixed Signal Processing	VL5L515	Mixed Signal Processing	Yes	-
	NVLL516	Computer Communication Networks	VL5L516	Computer Communication Networks	Yes	-
	NVLL517	Computer Graphics	CSEL517	Computer Graphics Applications	Yes	-

Fourth Semester M-Tech. VLSI

Branch:- Electronics Engineering

Sr. No.	Subject Code	Name of Subject as per R.T.M.N.U. Syllabus.	Subject Code	Name of Subject as per G.H.R.C.E. Syllabus Under Autonomous Status.	Equivalence	Remarks
1	NVLP518	Thesis / Dissertation (Viva Voce)	VLSP520	Thesis / Dissertation (Viva voce)	Yes	-

Sr. No.	Subject Code	Name of Subject as per R.T.M.N.U. Syllabus.	Subject Code	Name of Subject as per G.H.R.C.E. Syllabus Under Autonomous Status.	Equivalence	Remarks
1	NEXL502 NEXP502	VLSI Circuits	ELXL502 ELXP502	VLSI Circuits	Yes	-
2	NVLL504 NVLP504	Advanced Digital Signal Processing	VL5L504 VLSP504	Advanced Digital Signal Processing	Yes	-
3	NEXL503	Fuzzy Logic & Neural Networks	ELXL503	Soft Computing & Applications	Yes	-
4	NEXL504 NEXP504	Programming & Data Structures	--	--	-	Exempted
5	NEXL501	Digital Communication	--	--	-	Exempted
6	-	-	ELXL501	Data Communication Networks	Equivalent to NEXL506 Data Communication of II nd Sem in RTMNU	-
7	-	-	VL5L505	Embedded Systems-I	Equivalent to NEXL507 Microcontroller & Their Applications of II nd Sem in RTMNU	-

EXEMPTED COURSES

Sr. No.	Subject Code	Name of Subject
1	NEXL504 NEXP504	Programming & Data Structures
2	NEXL501	Digital Communication

Sr. No.	Subject Code	Name of Subject as per R.T.M.N.U. Syllabus.	Subject Code	Name of Subject as per G.H.R.C.E. Syllabus Under Autonomous Status.	Equivalence	Remarks
1	NEXL505 NEXP505	Digital System Design	ELXL507 ELXP507	Advanced Digital System Design	Yes	-
2	NVLL509 NVLP509	Digital Image processing	VLSL509 VLSP509	Advance Digital image Processing	Yes	-
3	--	--	ELXL508	Wireless Networks	-	Audit Course
4	--	--	VLSL512	Embedded Systems-II	-	Audit Course
5	NEXL506	Data Communication	-	-	Equivalent to ELXL501 Data Communication Networks of I st Sem Autonomous	-
6	NEXL507	Microcontroller & Their Applications	-	-	Equivalent to VLSL505 Embedded Systems-I of I st Sem Autonomous	-
7		Elective – I		Elective – I		
	NEXL508	Pattern Recognition	CSEL512	Pattern Recognition Techniques	Yes	-
	NEXL509	Analog VLSI Design	VLSL506	Analog VLSI Design	Yes	
	NEXL510	Computer Graphics	CSEL513	Computer Graphics Applications	Yes	-

AUDIT COURSES		
Sr. No.	Subject Code	Name of Subject
1	ELXL508	Wireless Networks
2	VLSL512	Embedded Systems-II