

**G.H. Raisoni College of Engineering, Nagpur**  
 Digdoh Hills Hingna Road Nagpur  
**TIME TABLE (CLASS WISE)**  
 Department of Electronics Engineering  
**MASTERS OF TECHNOLOGY(VLSI)**

W.e.f. Date:

Session:2019-2020						Year/Semester:I/I			
Section :A									
	8.30-9.30	9.30-10.30	10.30-11.30	11.30-12.30	12.30-1.30	1.30-2.30	2.30-3.30	3.30-4.30	4.30-5.30
MONDAY		ELECTIVE-1/ S.SALANKAR/E01	LIBRARY	ELECTIVE-3/ PIYUSH DHANDE/E18	R	GP I (Comm.+VLSI+IP S+CSE+CAD) <i>E04</i>	ES&C/DR.PADOL E/W22		
TUESDAY		ELECTIVE-2/E02	ELECTIVE-1/ S.SALANKAR/E01	GP I (Comm.+VLSI+IP S+CSE+CAD) <i>E03</i>	R	ELECTIVE-2/E02	ELECTIVE-3/ PIYUSH DHANDE/F03		
WEDNESDAY		VSD/PROF.S.MES HRAM/E09	GP I (Comm.+VLSI+IP S+CSE+CAD) <i>F-26</i>	ELECTIVE-1/ S.SALANKAR/E03	ELECTIVE-2/E02	R	ES&C/DR.PADOL E/E02		
THURSDAY		ELECTIVE-3/ PIYUSH DHANDE/E02	LIBRARY	GP I (Comm.+VLSI+IP S+CSE+CAD) <i>F-25</i>	ELECTIVE-1/ S.SALANKAR/E19	R	ES&C/DR.PADOL E/E04	LABATORY- I/DR.S.SAYAKAR/E46	
FRIDAY		ES&C/DR.PADOL E/E03	VSD/PROF.S.MES HRAM/E09	ELECTIVE-2/E08	R	ELECTIVE-3/ PIYUSH DHANDE/E04	VSD/PROF.S.MES HRAM/W22	LABATORY- I/DR.S.SAYAKAR/E46	
SATURDAY									

<b>Name of the subjects:</b>	ES&C-Embedded Systems & Controllers	VSD-VLSI subsystem Design	E-III-Machine Learning & its Engineering Applications
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**Time Table Incharge**

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26/8/15

**Head of the Department**

*[Signature]*

**Dean Academics**

*[Signature]*

**Director**